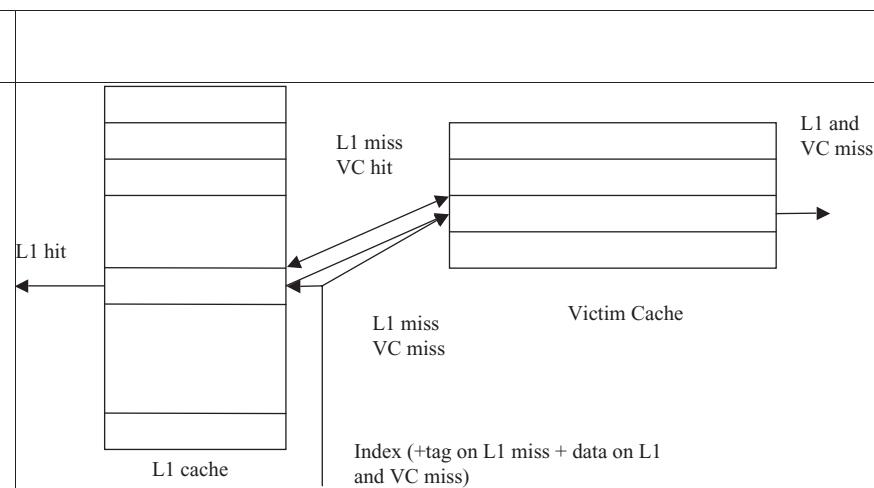


Convert Art

Figure 06-01.



Convert Art

Figure 06-02.

5

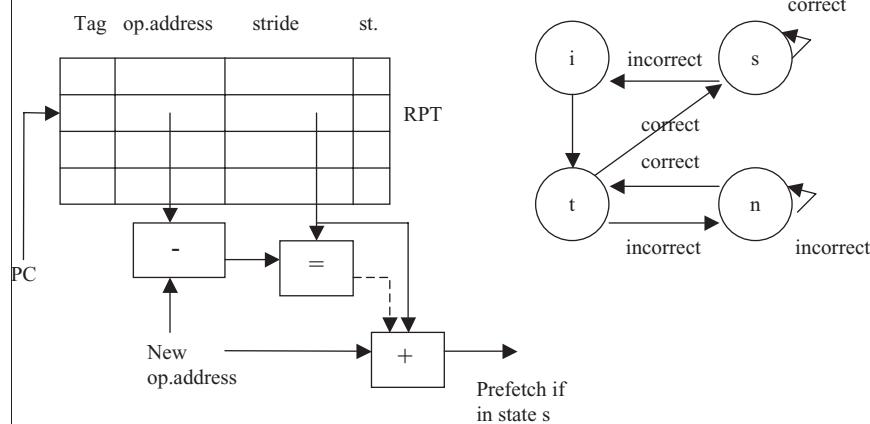
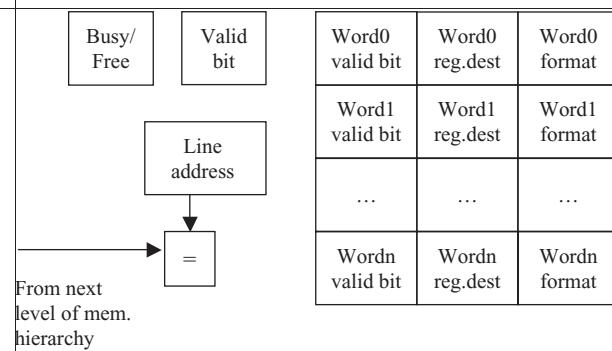


Figure 06-04.

Convert Art

7



Convert Art

Figure 06-05.

9

a'	b

L1: the LRU set is on the left

B	A'

(a) Initial contents

L2: the LRU set is  
on the left

a	inv

L1: the LRU set is  
on the right

A	A'

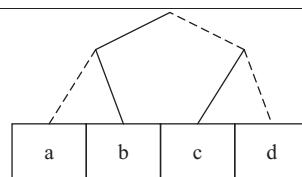
(b) contents after miss to a

L2: the LRU set is  
on the right

convert Art

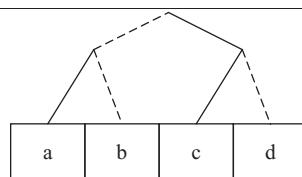
Figure 06-06.

II



(a) Corresponds to a cache initially empty and the string of references "bcad".

-----  
MRU path



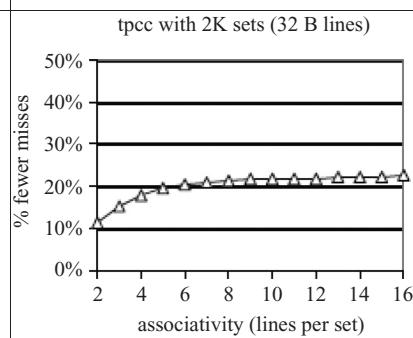
(b) The next reference is to "b". If the next reference is a miss, "c" will be the victim

In this example, the algorithm gives the same result as true LRU. But the same tree would have been built if the original reference stream were "bacd" and "c" would still have been replaced while in true LRU it would have been "a".

Convert Art

Figure 06-07.

13

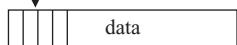


Convert + Edit  
Art

Figure 06-08.

15

Status bits, in particular valid bit



tag	Subblock1			subblockn

Convert Art

Figure 06-09.

17

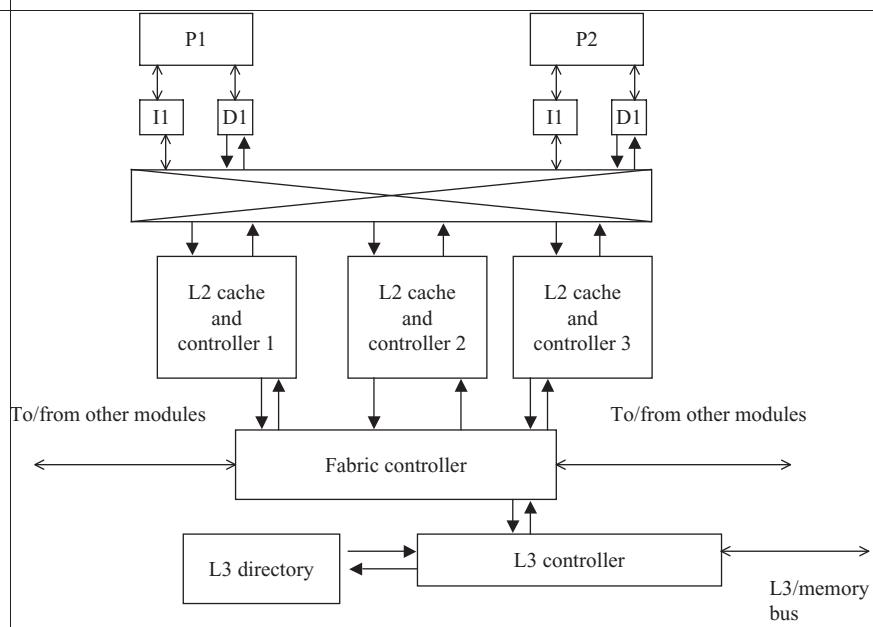


Figure 06-10.

Convert Art

19

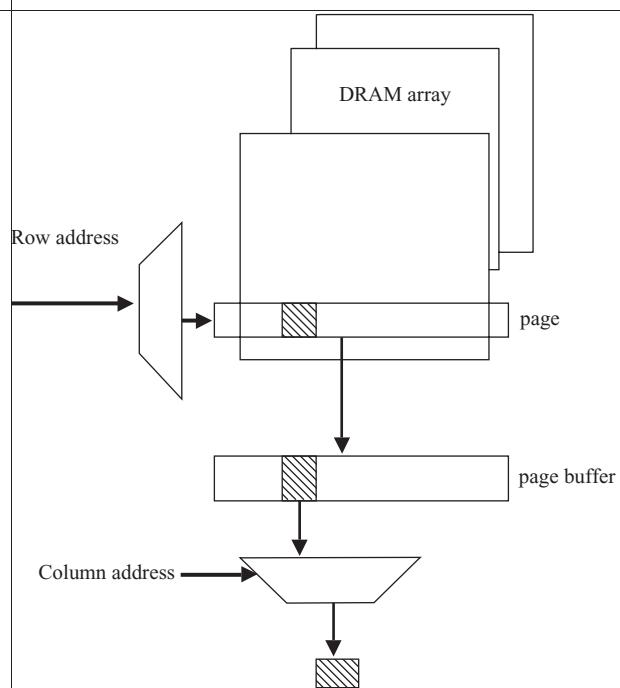
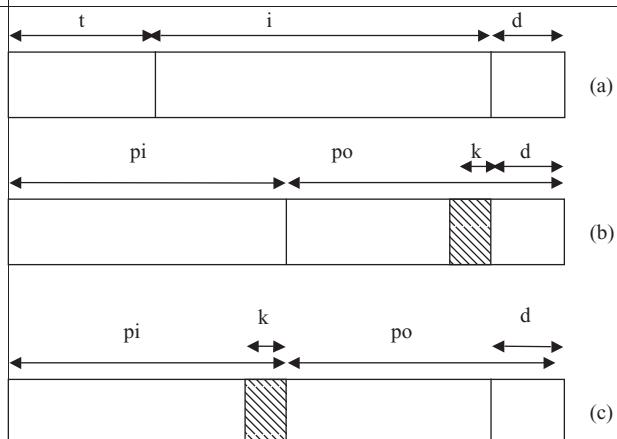


Figure 06-11.

Convert Art

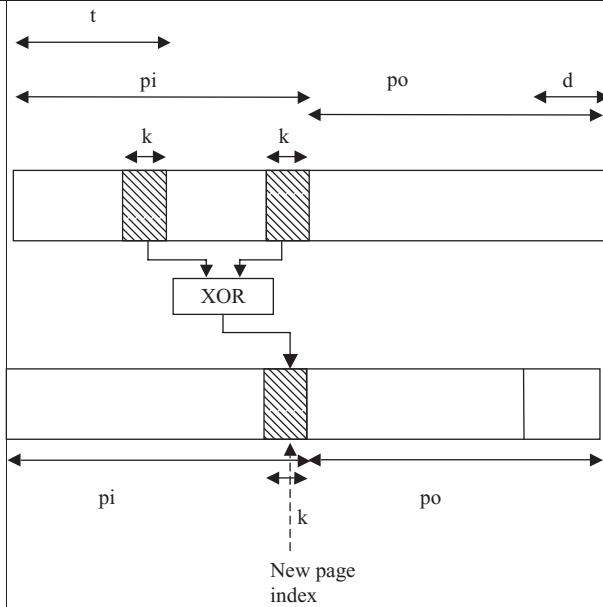
21



Convert Art

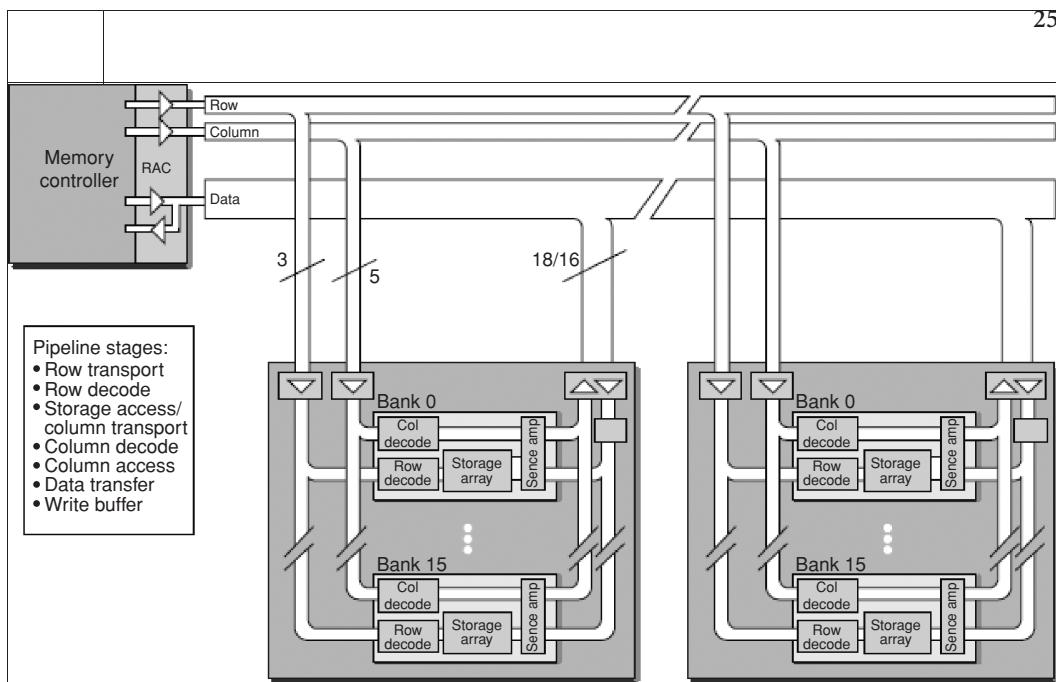
Figure 06-12.

23



Convert Art

Figure 06-13.



Convert Art

Figure 06-14.